











TAS2555 SLASE98 – AUGUST 2015

TAS2555 5.7-W Class-D Mono Audio Amplifier with Class-H Boost and Speaker Sense

1 Features

- Ultra Low-Noise Mono Boosted Class-D Amplifier
 - 5.7 W at 1% THD+N and 6.9 W at 10% THD+N into 4-Ω Load from 4.2-V Supply
 - 3.8 W at 1% THD+N and 4.5 W at 10% THD+N into 8-Ω Load from 4.2-V Supply
- Output Noise for DAC + Class-D(ICN) is 15.5 μV
- DAC + Class-D SNR 111 dB at 1%THD+N/8 Ω
- THD+N –90 dB at 1 W / 8 Ω with Flat Frequency Response
- PSRR 110 dB for 200 mV_{pp} ripple at 217 Hz
- Input Sample Rates from 8 kHz to 96 kHz
- Built-In Speaker Sense
 - Measures Speaker Current and Voltage
 - Measures VBAT Voltage, Chip Temperature
- Dedicated Real-Time DSP for Speaker Protection
 - Thermal and Excursion Protection
 - Detects Leak and Damaged Speaker
- High Efficiency Class-H Boost Converter With Multi-Level Tracking
 - 86% at 500 mW in 8 Ω with 3.6 V V_{BAT}
 - 87% at 700 mW in 8 Ω with 4.2 V V_{RAT}
- Built-In Automatic Gain Control (AGC)
 - Limits Battery Current Consumption
- Adjustable Class-D Switching Edge-Rate Control
- Power Supplies

Boost Input: 2.95 V to 5.5 V

Analog/Digital: 1.65 V to 1.95 V

Digital I/O: 1.62 V to 3.6 V

- Thermal, Short-Circuit, and Under-Voltage Protection
- I2S, Left-Justified, Right-Justified, DSP, and TDM Input and Output Interface, PDM Input
- I²C or SPI Interface for Register Control
- 3.47 mm x 3.23 mm, 0.5 mm pitch, 42-ball WCSP
- Stereo Configuration Using Two TAS2555 Devices

2 Applications

- Mobile Phones
- Tablets
- Portable Audio Docks
- Bluetooth Speakers

3 Description

The TAS2555 is a state-of-the-art Class-D audio amplifier which is full system on a Chip (SoC). The device features a ultra low-noise audio DAC and Class-D power amplifier which incorporates speaker voltage and current sensing feedback. An on-chip, low-latency DSP supports Texas Instruments SmartAmp(c) speaker protection algorithms to maximizes loudness while maintaining safe speaker conditions.

The device can be used easily with any processor with an I2S output and stereo implementations are possible when using two TAS2555 devices. Separate tuning for different speakers is supported allowing customers to add value while maintaining form factor designs. Additionally, the TAS2555 supports separate voice and audio tuning dynamically with ultra-low 15.5 µV ICN regardless of mode of operation making receiver/speaker implementations possible.

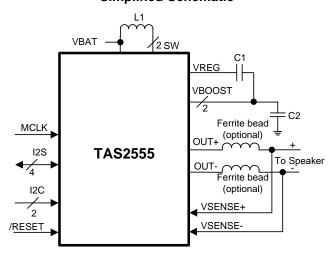
A Class-H boost converter generates the Class-D amplifier supply rail. When the audio signal only requires a lower Class-D output power, the boost improves system efficiency by deactivating and connecting V_{BAT} directly to the Class-D amplifier supply. When higher audio output power is required, the multi-level boost quickly activates tracking the signal to provide the additional voltage to the load.

Device Information⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
TAS2555	WCSP (42)	3.47 mm x 3.23 mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Simplified Schematic





4 Revision History

DATE	REVISION	NOTES
August 2015	*	Initial release.



5 Device and Documentation Support

5.1 Documentation Support

5.2 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E™ Online Community TI's Engineer-to-Engineer (E2E) Community. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

Design Support *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

5.3 Trademarks

E2E is a trademark of Texas Instruments.

All other trademarks are the property of their respective owners.

5.4 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

5.5 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

6 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

6.1 Package Dimensions

The TAS2555 uses a 42-ball, 0.5 mm pitch WCSP package. The die length (D) and width (E) correspond to the package mechanical drawing at the end of the datasheet.

Product Folder Links: TAS2555



PACKAGE OPTION ADDENDUM

19-Aug-2015

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish (6)	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
TAS2555YZR	PREVIEW	DSBGA	YZ	42	3000	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	-40 to 85	2555	
TAS2555YZT	PREVIEW	DSBGA	YZ	42	250	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	-40 to 85	2555	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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PACKAGE OPTION ADDENDUM

19-Aug-2015

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